

T. E. Sem-VI (R-2007) EX-^{EPRX} — ^{EPUC-II} 2911156
 (old)

Q. P. Code : 590403

(3 Hours)

[Total Marks: 100]

- Note: 1. Question No. 1 is compulsory.
 2. Solve any four from the remaining questions.
 3. All questions carry equal marks.

1. a. Explain the architecture of the 8086 microprocessor with a neat diagram. Highlight the function of each block. (10)
 b. Explain the addressing modes of the PIC 18 microcontroller with neat examples. (10)
 2. a. Explain the minimum and maximum mode operation of the 8086 microprocessor. (10)
 b. Interface the 8086 microprocessor to the 8255 programmable peripheral interface (PPI) and explain the various interfacing signals. (10)
 3. a. Explain with a neat diagram the 8086 -8087 math coprocessor interface. Highlight the function of the Request/Grant pins. (10)
 b. Explain the memory organization of the PIC 18. (10)
 4. a. Write a program to perform multiplication of two numbers using rotate instructions of the PIC 18. (10)
 b. Write a detailed note on assembler directives of the 8086 highlighting the function of each. (10)
 5. a. Write a program to transfer a block of data from one memory location to another using the string instructions of the 8086. (10)
 b. Compare the salient features of 8086, 80386 and the Pentium. (10)
 6. a. Explain the advantages of Segmentation w.r.t. the 8086 processor. (10)
 b. Interface 4KB of ROM and 4KB of RAM to the 8086. Show the address decoding and memory map. (10)
 7. Write short notes on (Any two): (20)
 - a. 8288 Bus controller
 - b. Parallel ports of the PIC 18
 - c. 8257 DMA controller
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