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Q.P. Code: 583701

	•	(3 Hours)	[ Total Marks : 100
N.B. :	(1) Question no <b>ONE</b> is <b>c</b>	ompulsory	

N.B. :	(1)	Question	no ONE	is	compulsory

- (2) Attempt any FOUR from the remaining six questions.
- (3) Assume suitable data if required.
- (4) Figure to the right indicates full marks.

1	(a)	Differentiate 8 bit and 16 bit microprocessor.	-
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	(b)	Explain control units of 8085 microprocessor.	5
	(c)	Explain memory banking in 8086 microprocessor.	5
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	(d)	Explain assembler and assembler directives.	5

- 2. (a) With respect to microprocessor, explain what is instruction cycle, machine cycle and T-state. Draw the timing diagram of op-code fetch machine cycle.
  - Explain with the help of block diagram, the minimum mode operations (b) 10 of 8086 microprocessor.
- Write a program to find the largest number in a set of 10 numbers for 3. (a) 10 8086 microprocessor.
  - Explain the following with respect to 8086 Microprocessor. (b) 10
    - (i) Pipelining
    - (ii) M/<del>I</del>O
    - (iii) Indexed addressing mode
- 4. (a) Explain all bit oriented instructions of 8051 microprocessor. What is power down mode and sleep mode.
  - Explain memory organization of 8051 microprocessor with the help of suitable diagram.
- Write instructions to initialize 8255 PPI for mode 2 operation. 5. (a) 10Explain mode 2 operation of 8255 PPI.
  - Write a program to generate square wave of time periods 1 msec with 10 (b) 50% duty cycle using 8254 interval timer and counter.

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. 6. (a)	Explain all bit oriented instructions of 8051 microcontroller.  Explain the following terms related to microprocessor and

- (i) Program counter
- (ii) Sack and stack pointer
- (iii) Subroutine
- (iv) Ready and wait
- (v) Reset

microcontroller.

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7.	Write short note in any two	
		1. 65 1 11

- (i) Microcontroller based traffic signal control
- (ii) Memory segmentation in 8086
- (iii) DC motor control using microcontroller.

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