(3 Hours) [Total Marks :100 a2zSubjects.com N.B.: (1) Q.No 1 is computsory (2) Solve any three questions out of remaining questions. Assume suitable data if necessary. Solve any five 20 (a) Convert (i) (174.03125)₁₀ in octal number and (DB.94)₁₆ in binary (ii) Make subtraction using 2's complement method (52)₁₀ -(65)₁₀ Compare schottky barrier diode and PN junction diode (c) Derive the relation between α and β. (d) List the ideal characteristics of OPAMP Prove that NAND gate is universal gate. a2zSubjects.com Convert T-FF to D-FF (f) (a) Draw block diagram of a shunt voltage regulator and explain the working 4 Derive the expression for the stability factor 'S' of a voltage divider bias (b) 8 circuit Draw circuit diagram of differentiate using OPAMP and explain 8 (¢) Explain inverting summing amplifier using OPAMP. Derive the expression 3. (a) 8 for output voltage. $Y = ABC + B\overline{CD} + \overline{ABC}$ simplify this equation and realize using basic gates. (b) Minimize the following expression using K-map 8 (c) $Y = \sum m (1,2,9,10,11,14,15)$ Implement the circuit using minimum number of gates Design on 8 bit comparator using IC 7485 8 4. (a) Implement the following function using 8:1 Mux 4 (b) $F(A, B, C, D) = \sum m (0,1,2,4,6,9,12,14)$ What is shift register? Mention different modes of operation of shift register. (c)

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[TURN OVER

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5.	(a)	What are advantages of VHDL	8
		Write VHDL program for full adder	
	(b)	Design 4 bit synchronous up counter using T-FF	8
	(c)	Draw the circuit of JK FF using NAND gates and write the truth table	4
6.	(a)	Design on astable multivibrator using IC 555 timer to generate an output	5
		of 1KHz with 60% duty cycle	
	(b)	Draw the circuit diagram of regulated power supply to produce out put voltage of +5V	5
	(c)	Draw drain characteristics of n-channel JFET and explain various regions	5
	(d)	What is excess 3 code? Why it is called self complementary code?	5

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